



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/823,877  
Filing Date: 4-14-04  
Applicant: James Anderson et al.  
Group Art Unit: 2812  
Examiner: to be assigned  
Title: Method for Packaging Integrated Circuit Chips

Attorney Docket: NGC-00045DA (48-0023D)

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**STATUS LETTER**

Sir:

Please advise when we may expect an Office Action in the above-identified patent application, which was filed on April 14, 2004.

Respectfully submitted,

WARN, HOFFMANN, MILLER & LALONE, P.C.  
Attorneys for Applicant(s)

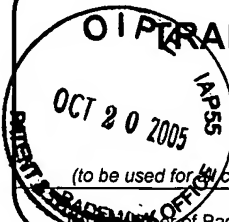
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By: John A. Miller  
John A. Miller  
Reg. No. 34985

Dated: 10-18-05

JAM/cac

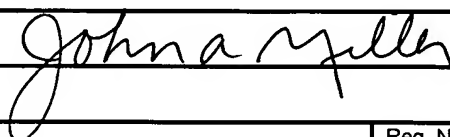
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	First Named Inventor	James Anderson et al.
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Number of Pages in This Submission		

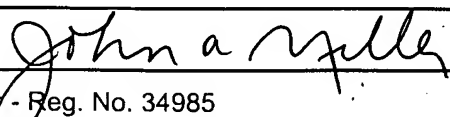
**ENCLOSURES** (Check all that apply)

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<b>Remarks</b> Applicant believes no fee to be due for the attached filing, however, should additional fees be due in order to prevent the abandonment of this application, please consider this as authorization to charge Deposit Account No. 501612 (Warn, Hoffmann, Miller & LaLone, P.C.) for any such fees due. A duplicate copy of this document is enclosed for this purpose.		

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name	Warn, Hoffmann, Miller & LaLone, P.C.		
Signature			
Printed name	John A. Miller		
Date	10-18-05	Reg. No.	34985

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:			
Signature			
Typed or printed name	John R. Miller - Reg. No. 34985	Date	10-18-05

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